

Figure 1

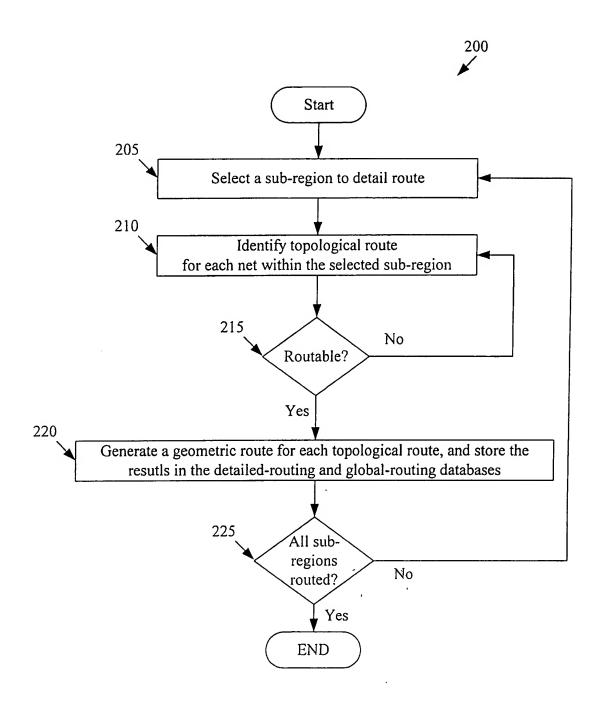
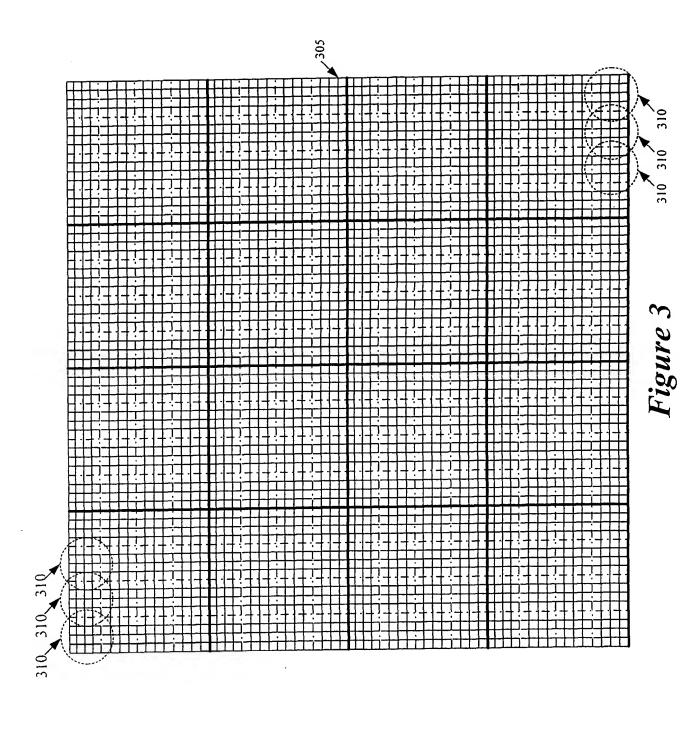


Figure 2



HOLDEN'S TOUR

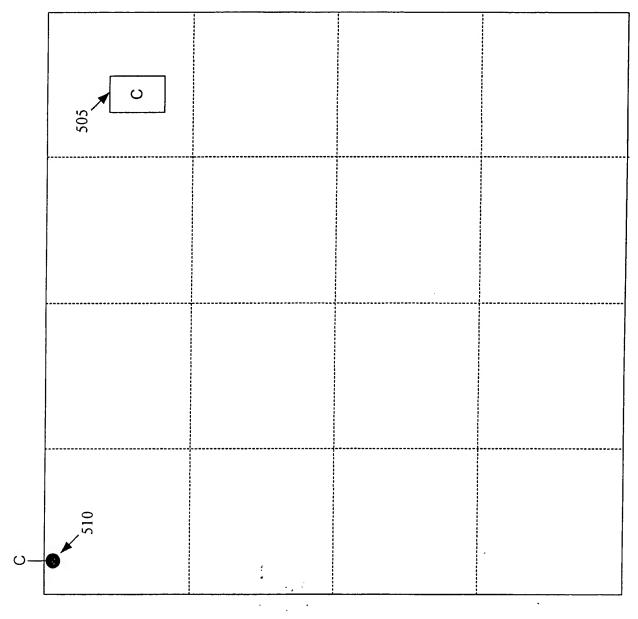


Figure 5

-List of Geometries

-- Each Geometry including a sequence of points & layer assignment

-Bounding box of the region

-Array of layer properties

--Minimum wire size

--Minimum spacing

--Via sizes

Figure 6

--Cost/Unit

-Netlist specifying a number of nets

-- Each net specifying a set of pins

-- Each port specifying a set of geometries -- Each pin specifying a set of ports

-List of Geometries

--Each Geometry including a sequence of points & layer assignment

--List of connection nodes inside each pin geometry

-Bounding box of the region

-Array of layer properties

--Minimum wire size

--Minimum spacing

--Via sizes

--Cost/Unit

-Netlist specifying a number of nets

-- Each net specifying a set of pins

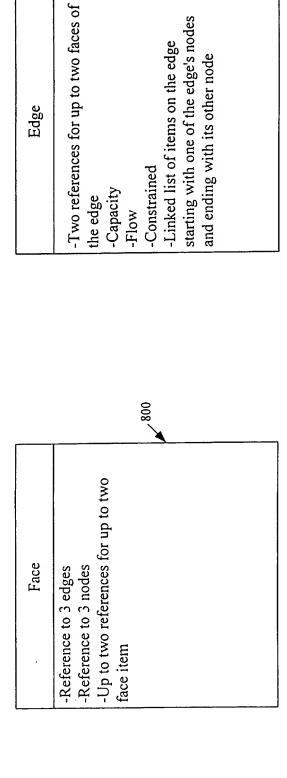
-- Each port specifying a set of geometries -- Each pin specifying a set of ports

-For each layer, a graph specifying

--Nodes

--Edges

--Faces

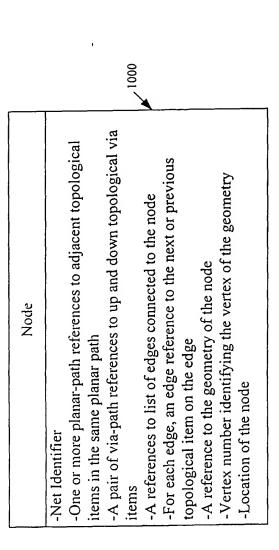


900

Edge

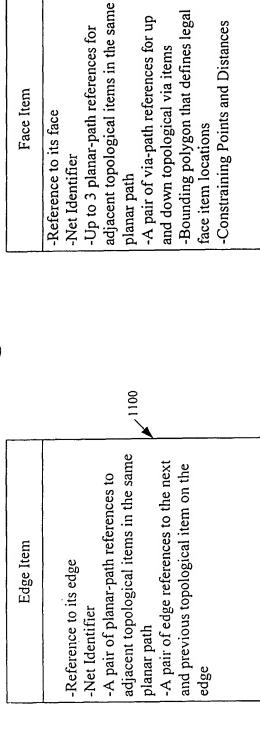
## Figure 8

## Figure 9



## Figure 10

Face Item



## Figure 12

Figure 11

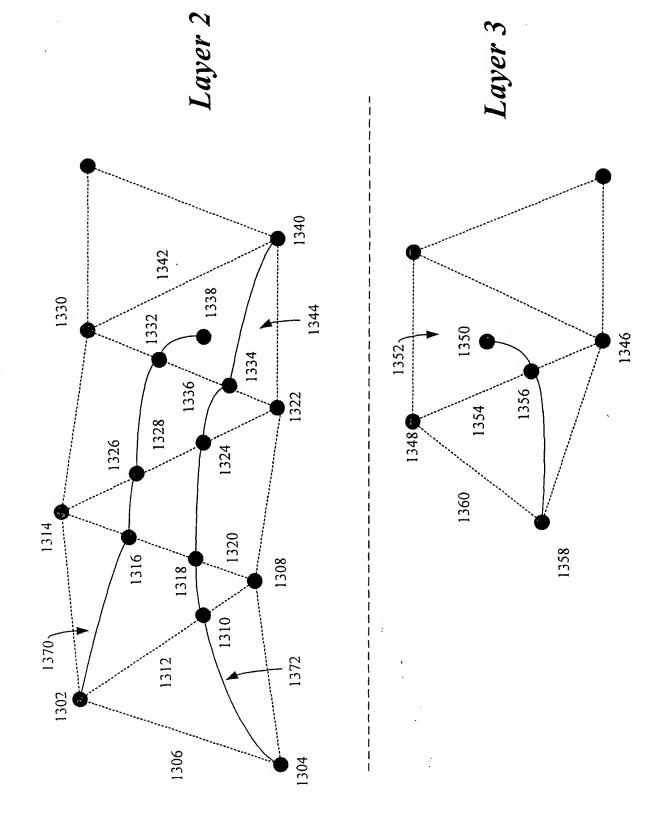
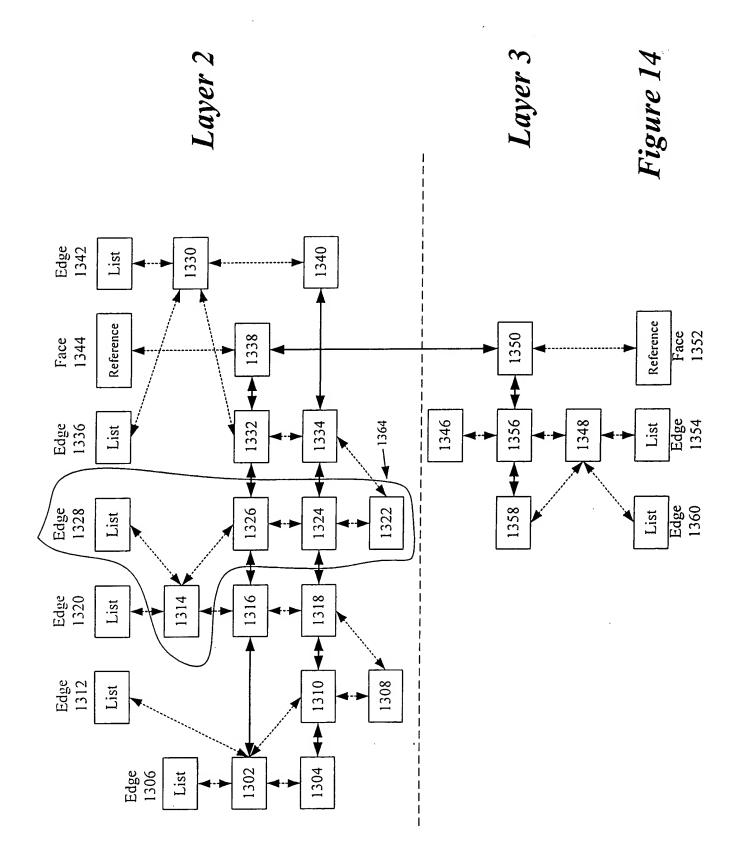
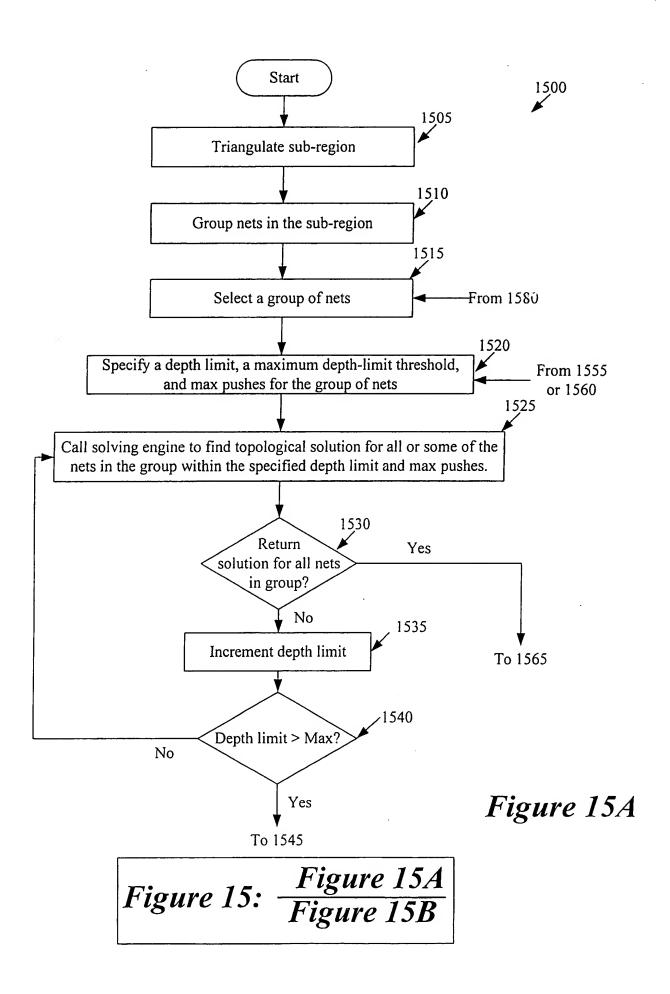
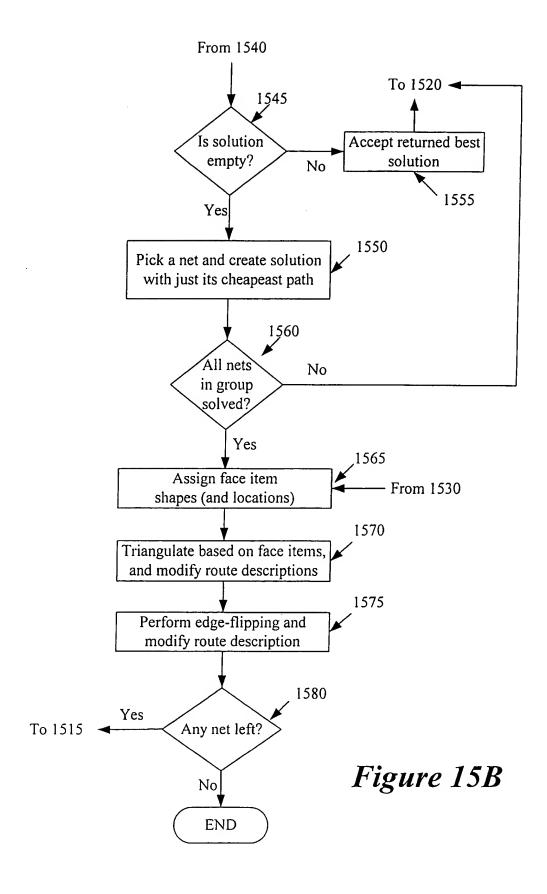
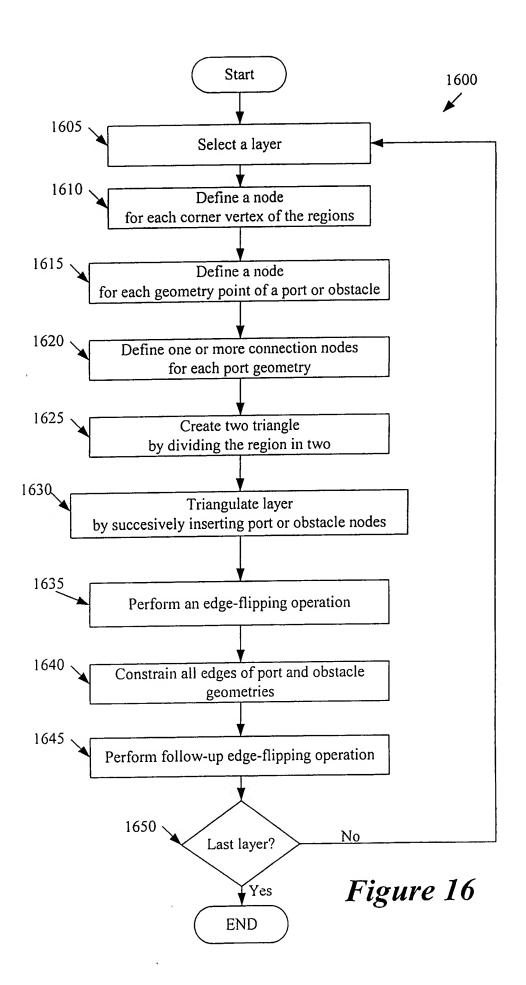


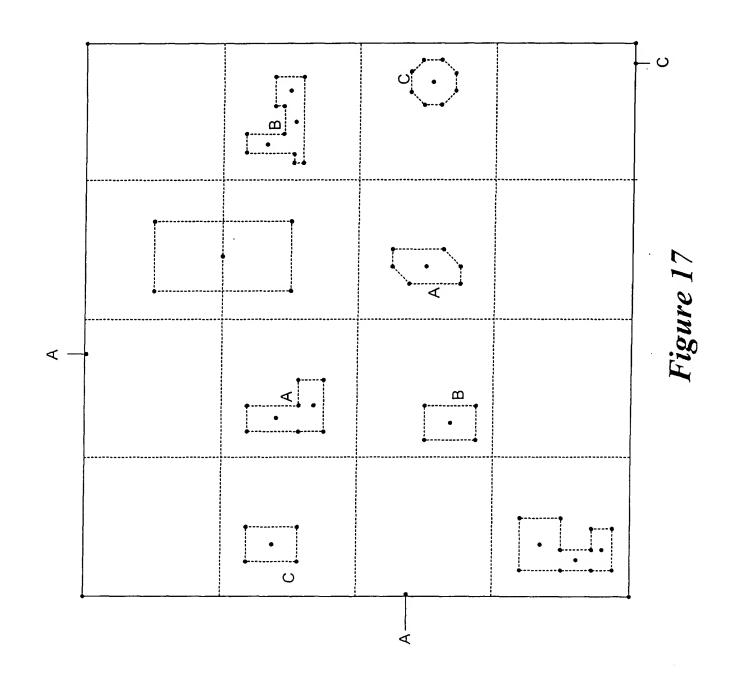
Figure 13











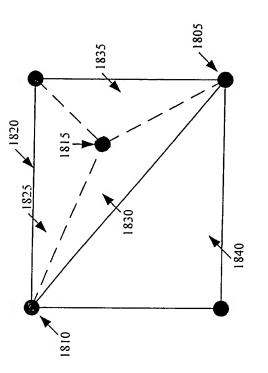


Figure 18

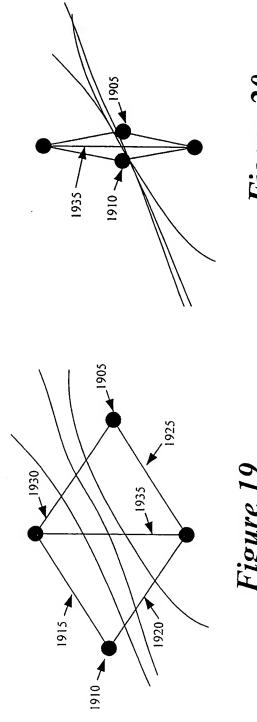
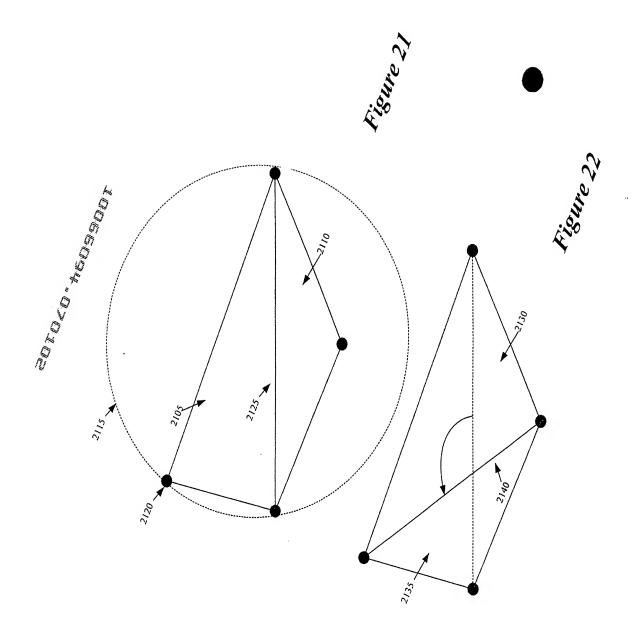
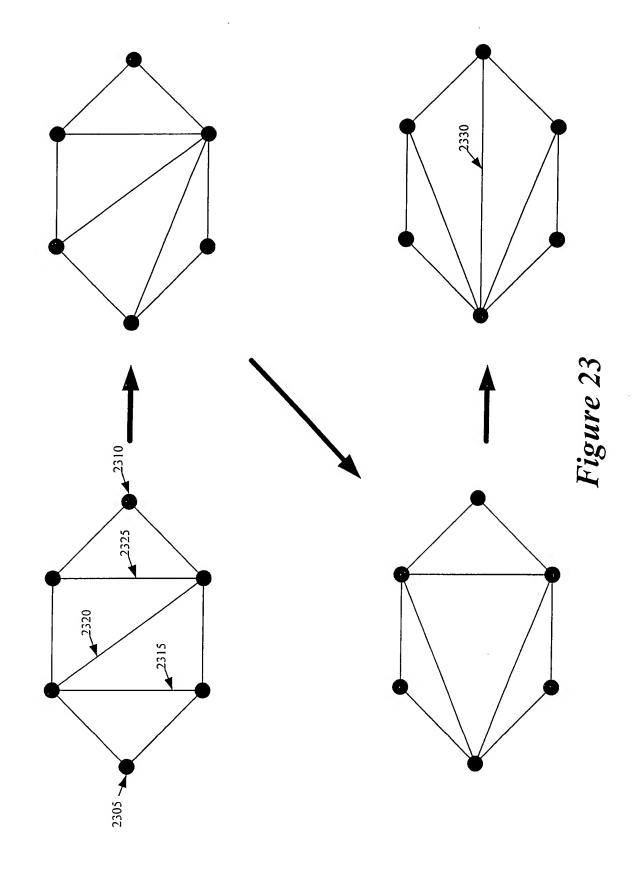


Figure 19

Figure 20





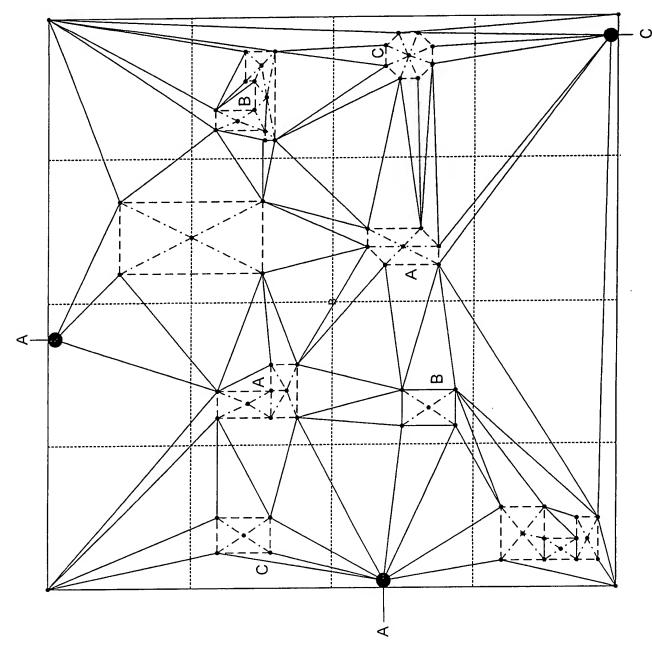


Figure 24

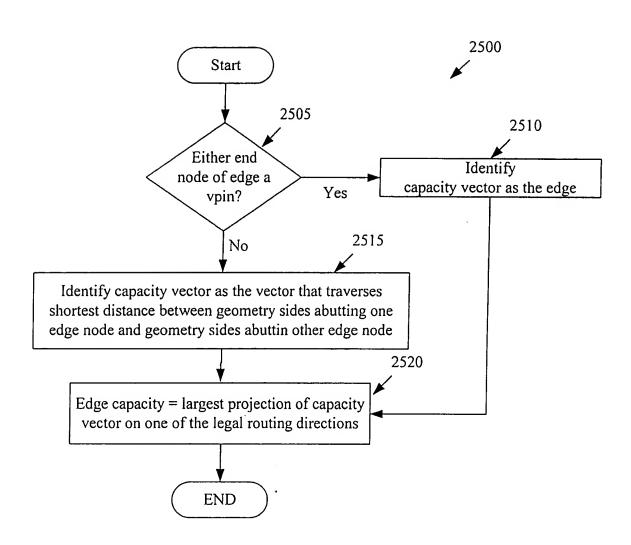


Figure 25

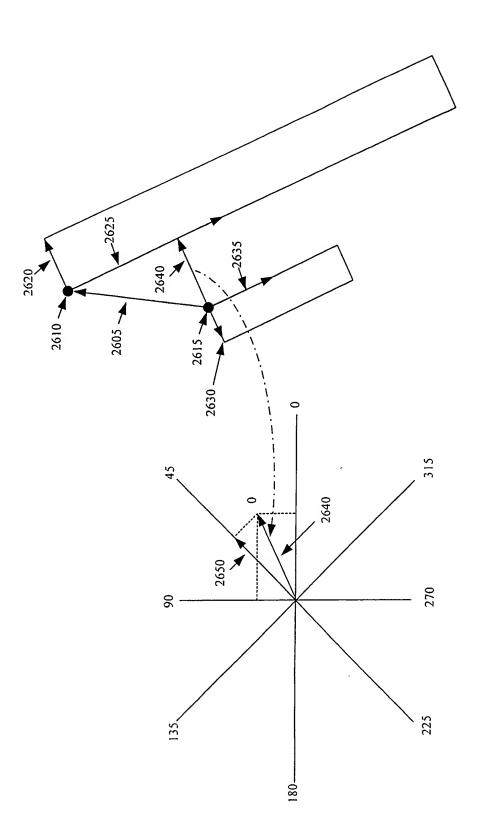


Figure 26

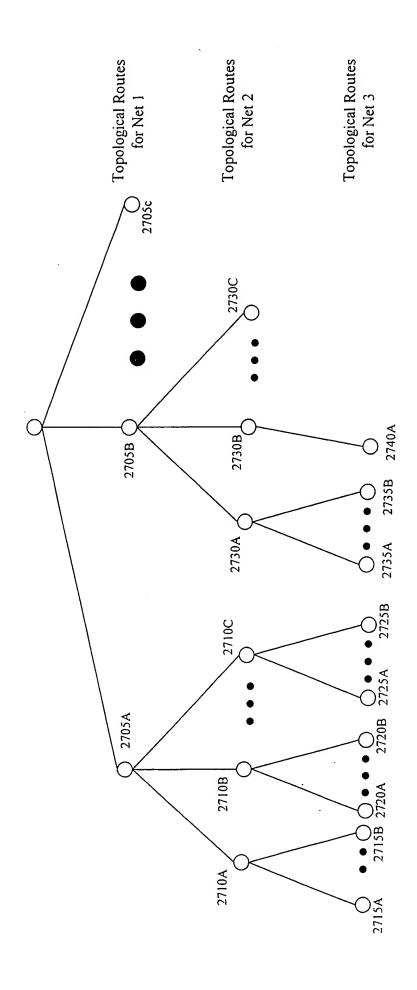


Figure 27

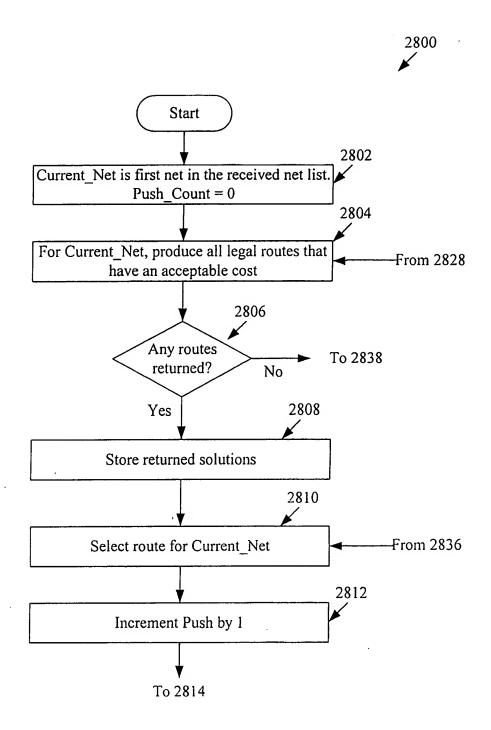
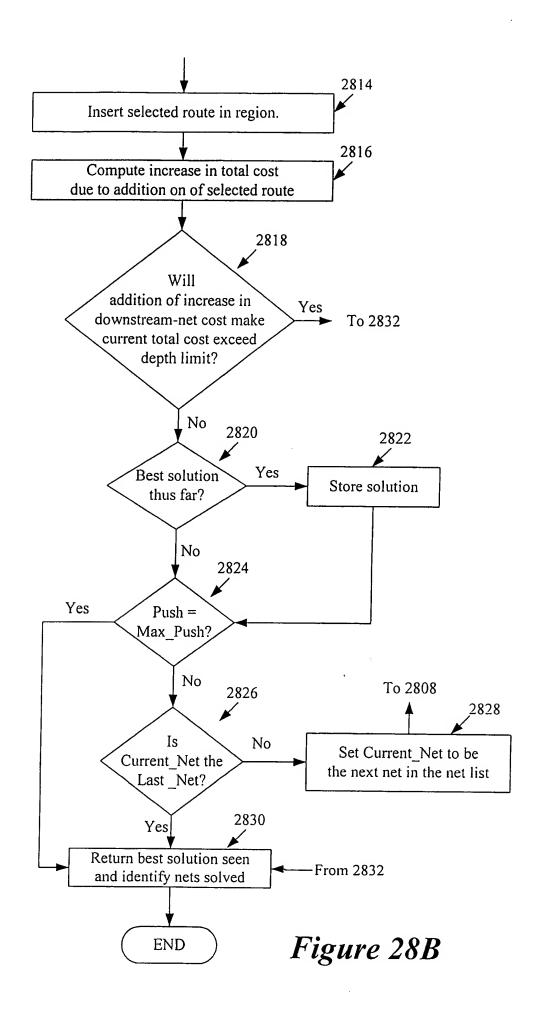


Figure 28A

Figure 28: Figure 28A
Figure 28B
Figure 28C



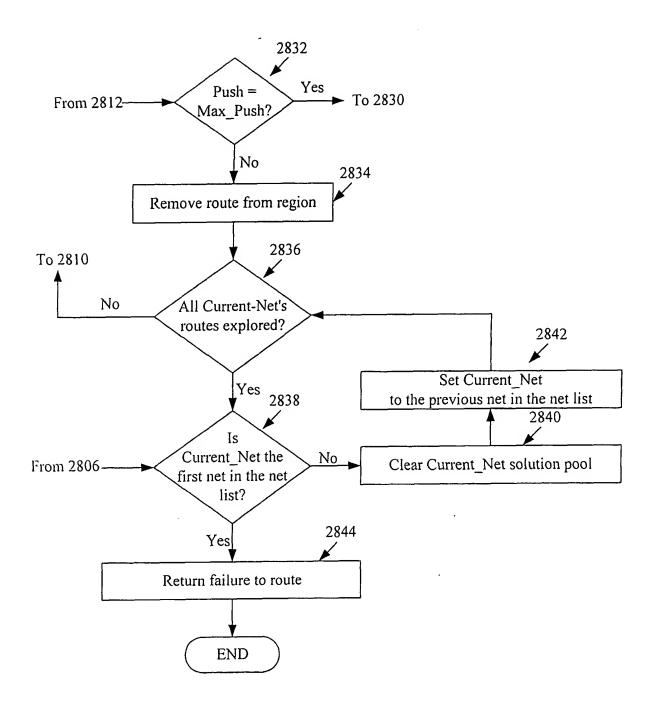


Figure 28C

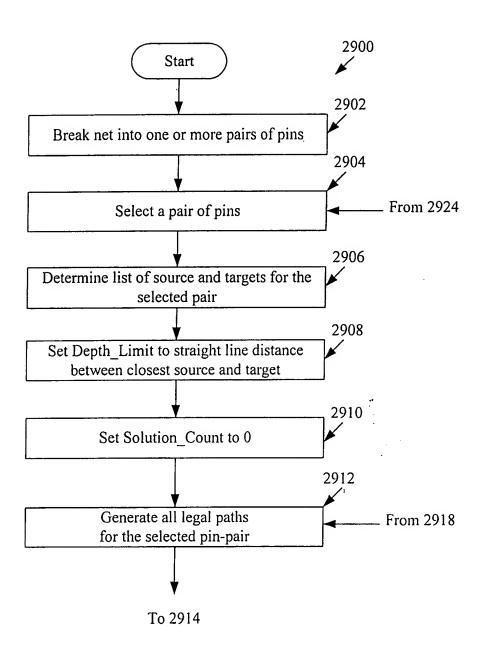
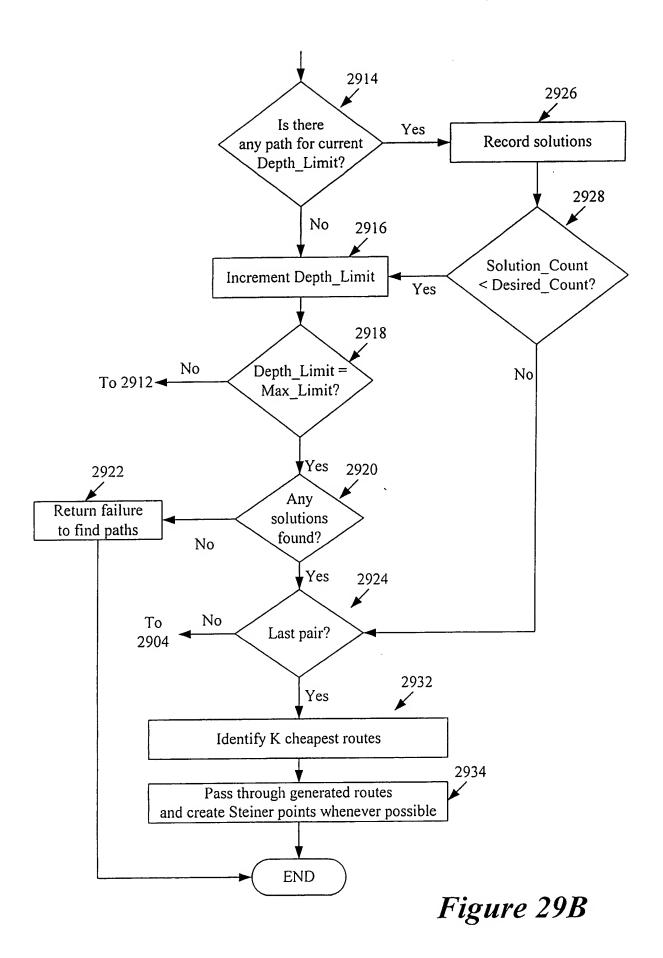
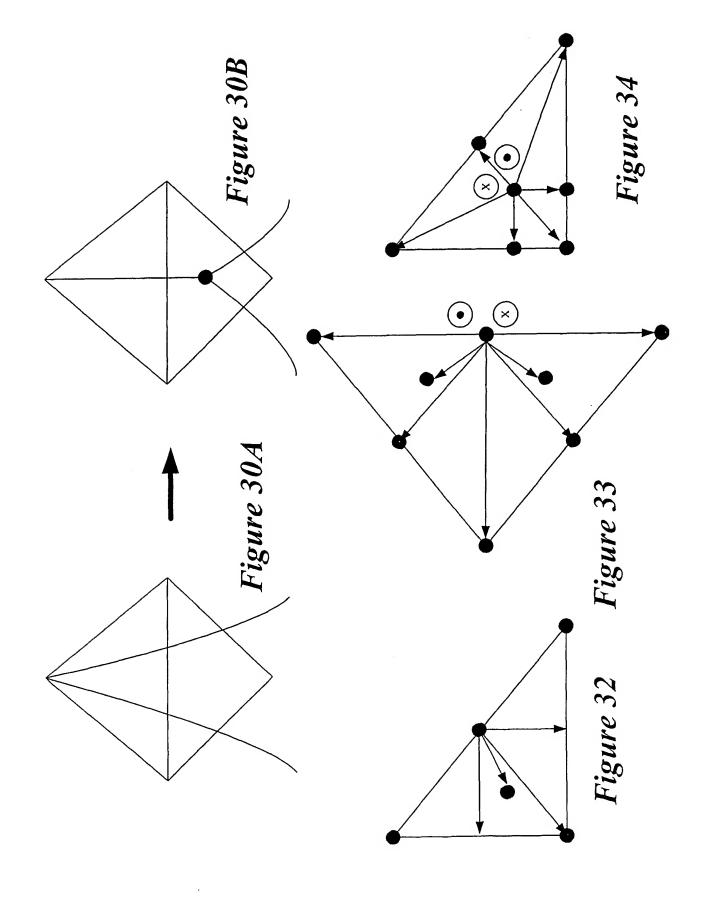


Figure 29A

Figure 29: Figure 29A Figure 29B





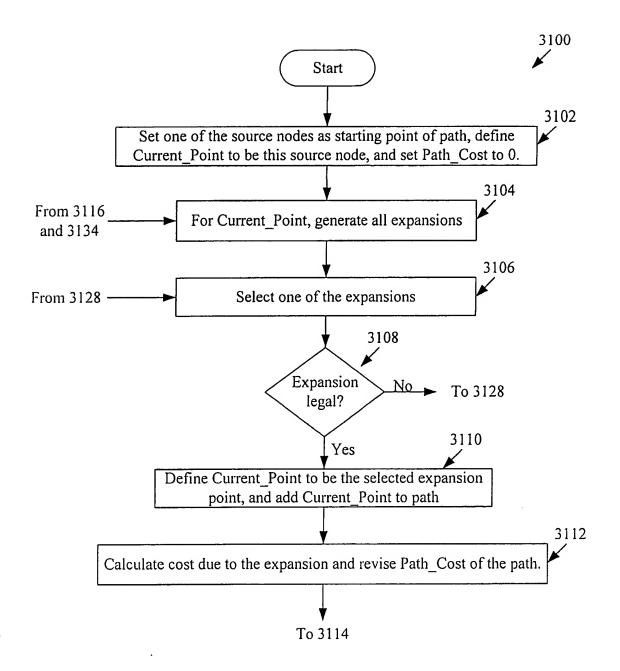
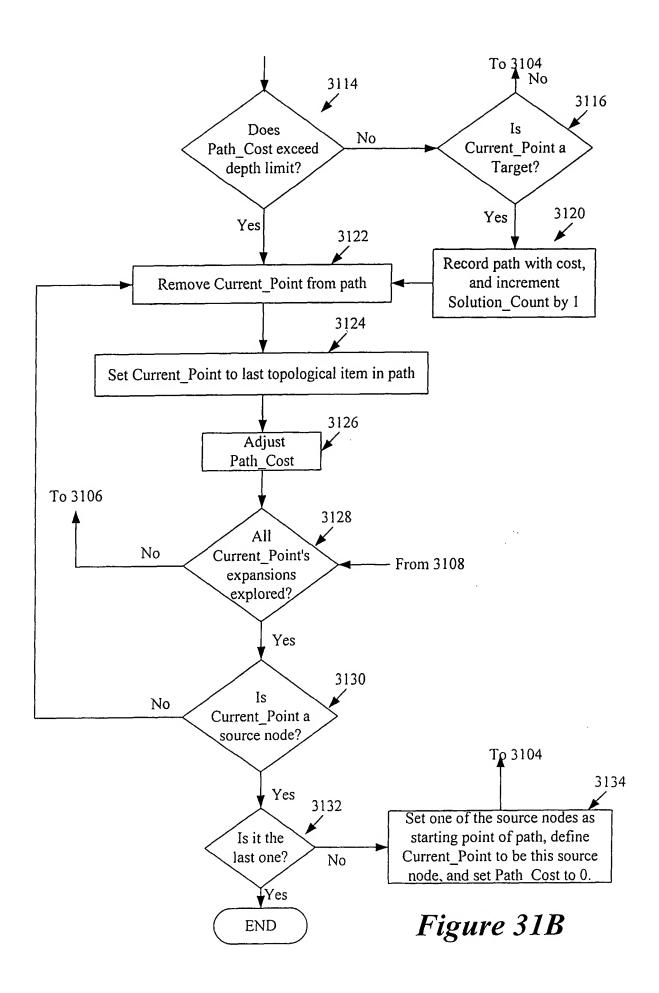
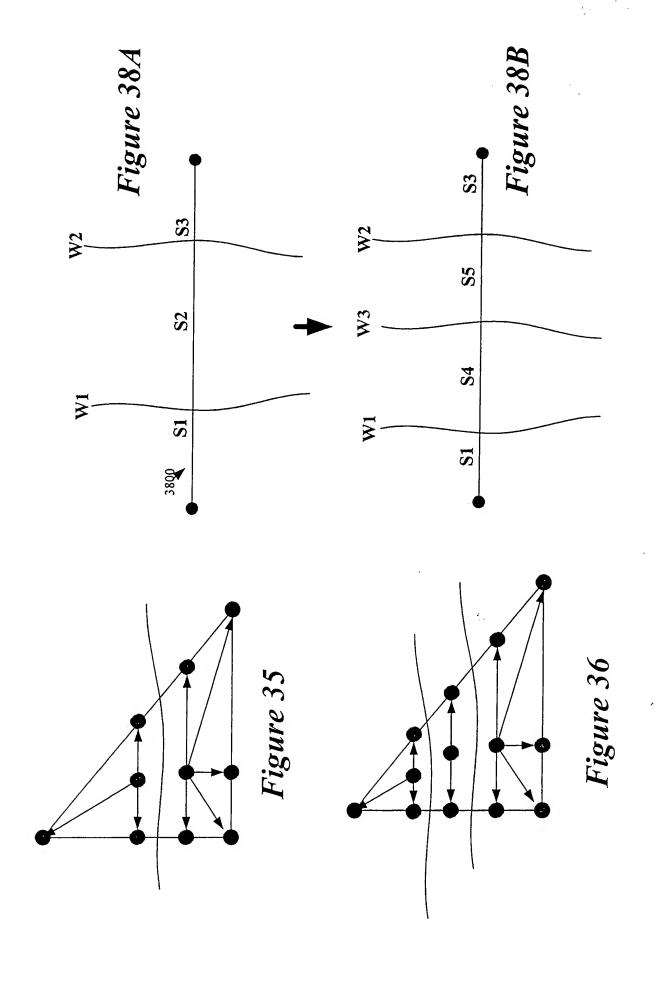


Figure 31A

Figure 31: Figure 31A Figure 31B





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ltem Edge Item	<ul><li>Planarity</li><li>Vias</li><li>Edge</li><li>Capacity</li></ul>	• Vias • Edge Capacity	<ul> <li>Planarity</li> <li>Vias</li> <li>Edge</li> <li>Capacity</li> </ul>
Face Item	Vias	Vias	• Vias
): Node	<ul><li>Planarity</li><li>Vias</li></ul>	• Vias	<ul><li>Planarity</li><li>Vias</li></ul>
To:	From: Node	Face Item	Edge Item

Figure 37

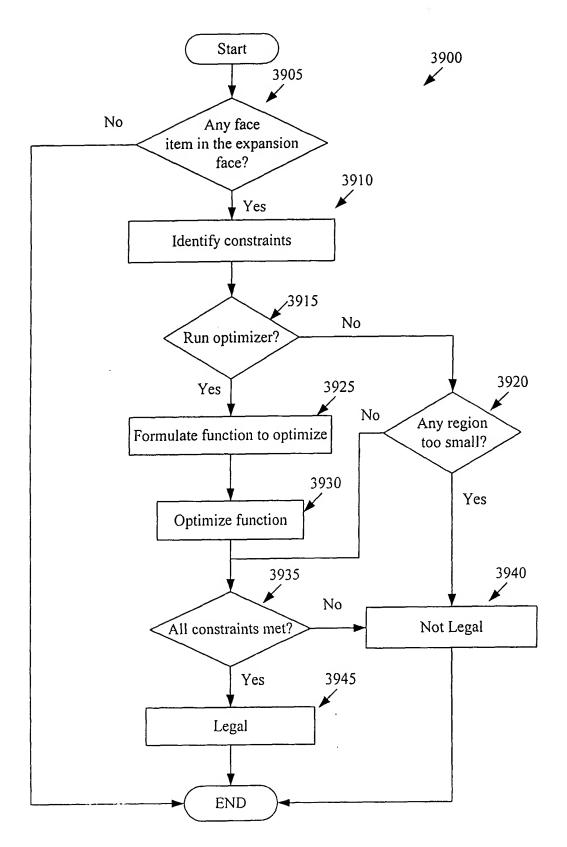


Figure 39A

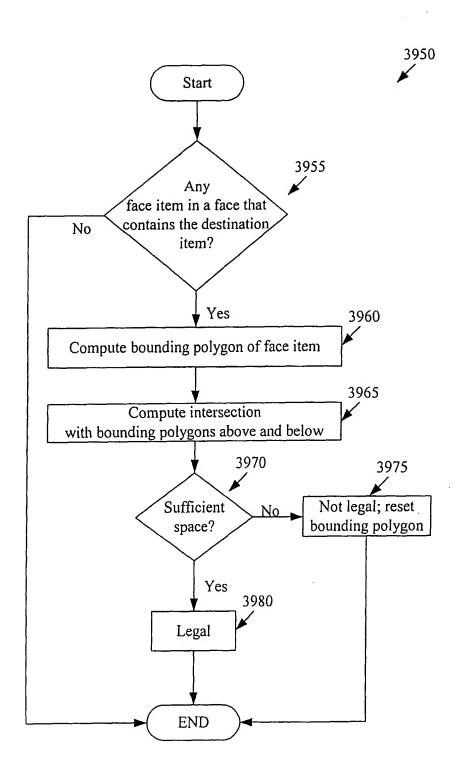
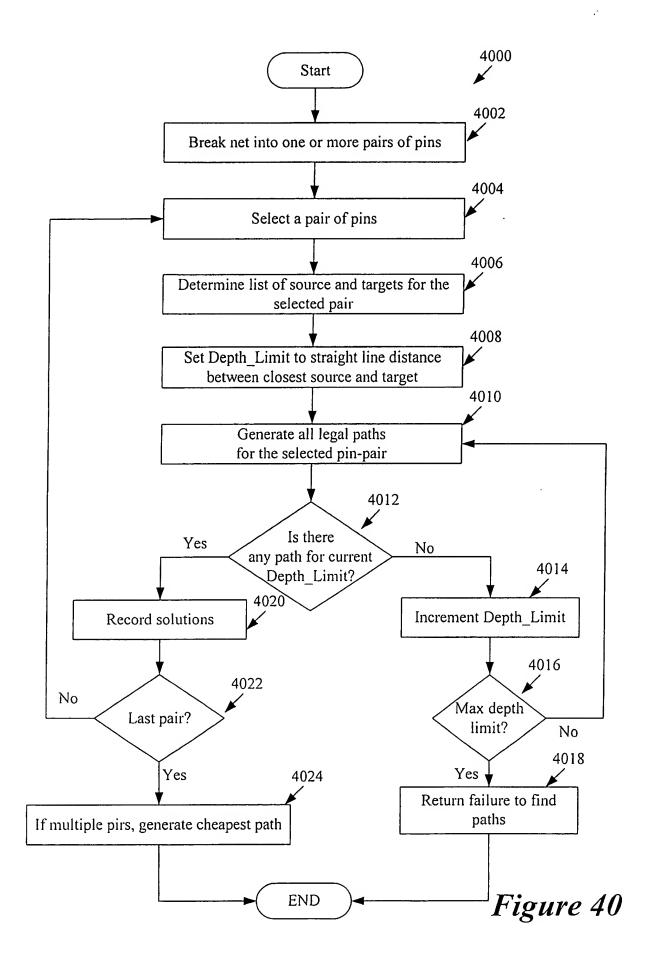


Figure 39B



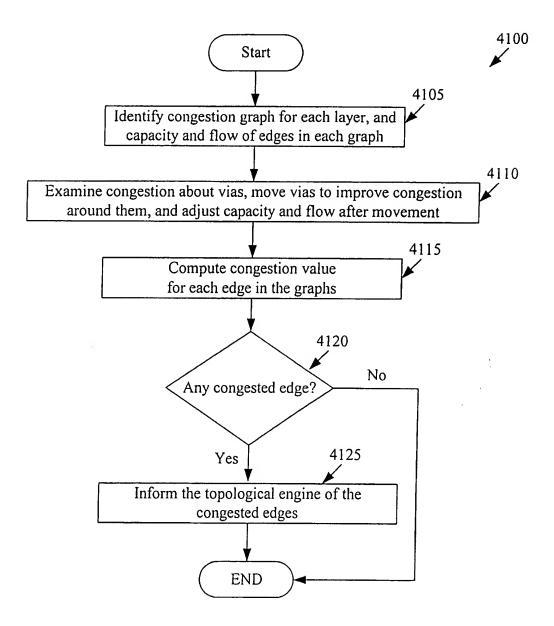


Figure 41

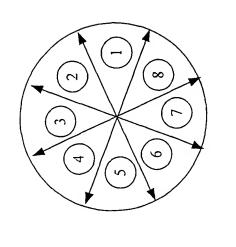


Figure 42

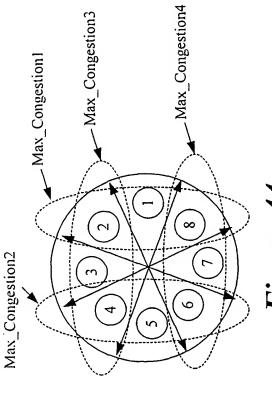


Figure 44

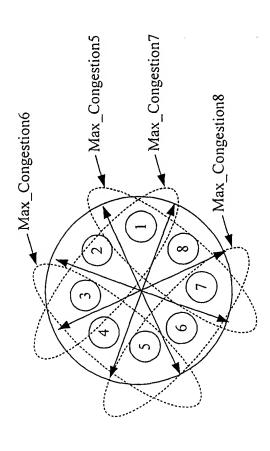


Figure 45

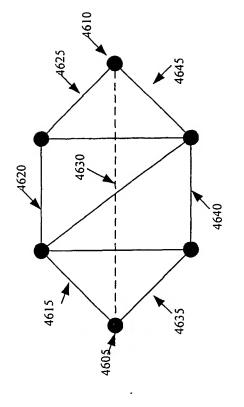
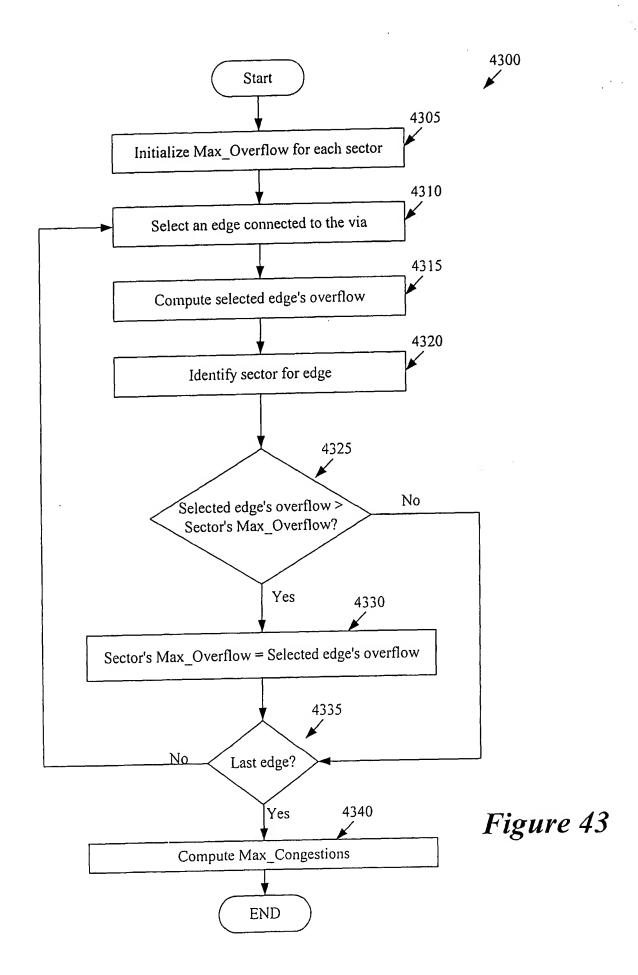


Figure 46



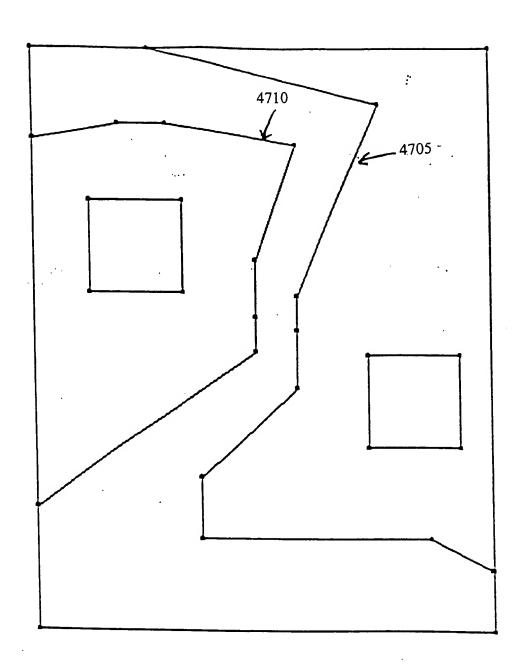


FIGURE 47

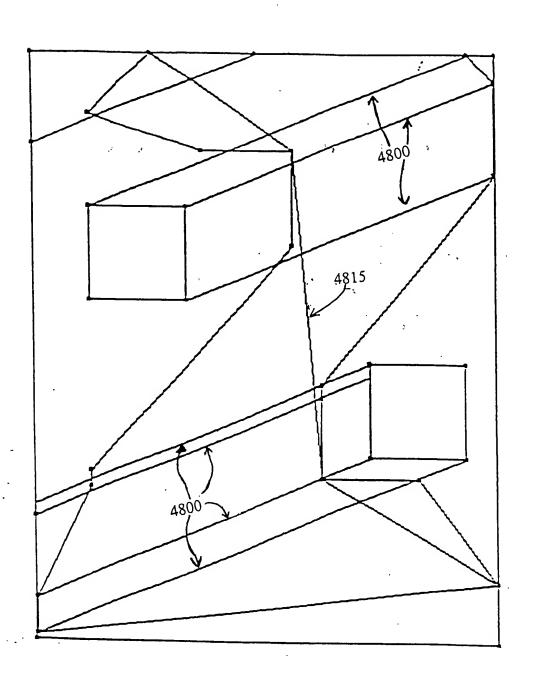


FIGURE 48A

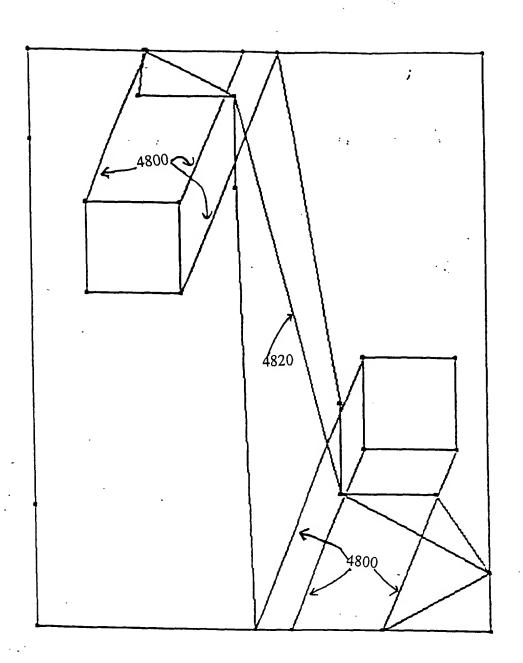


FIGURE 48B

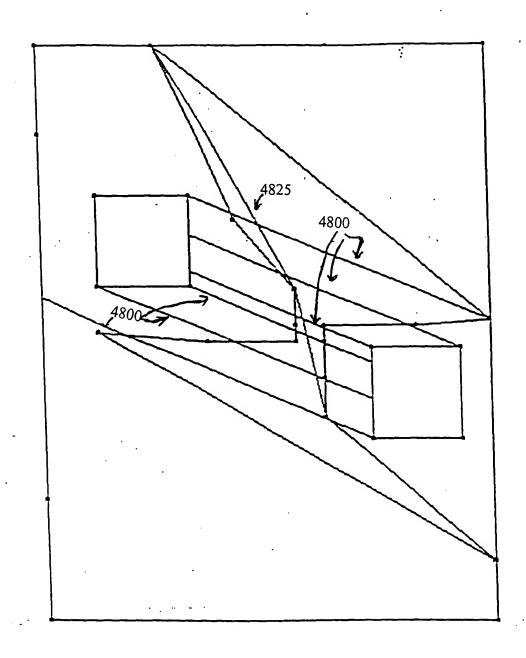


FIGURE 48C

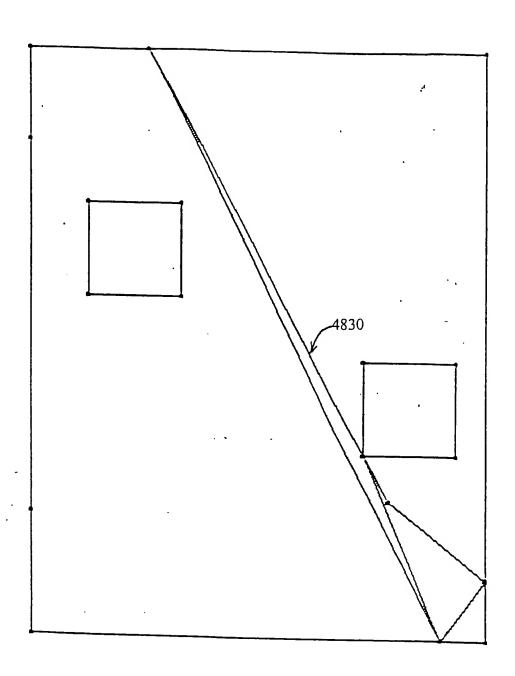


FIGURE 48D

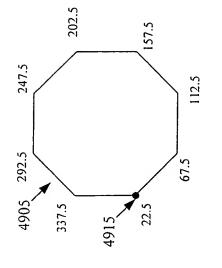


Figure 49A

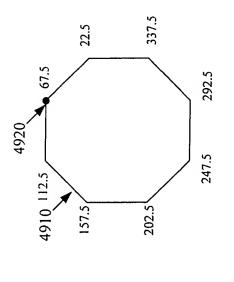


Figure 49B

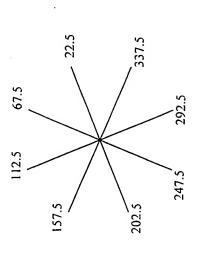


Figure 49C

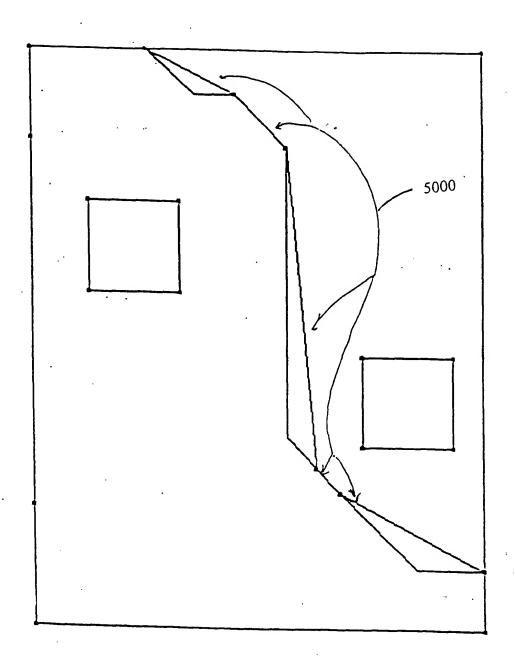


FIGURE 50

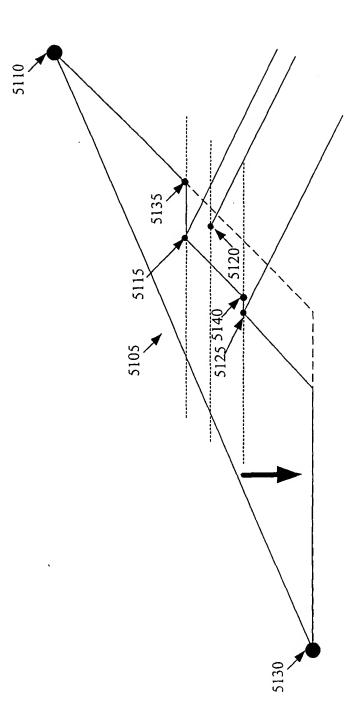


Figure 51

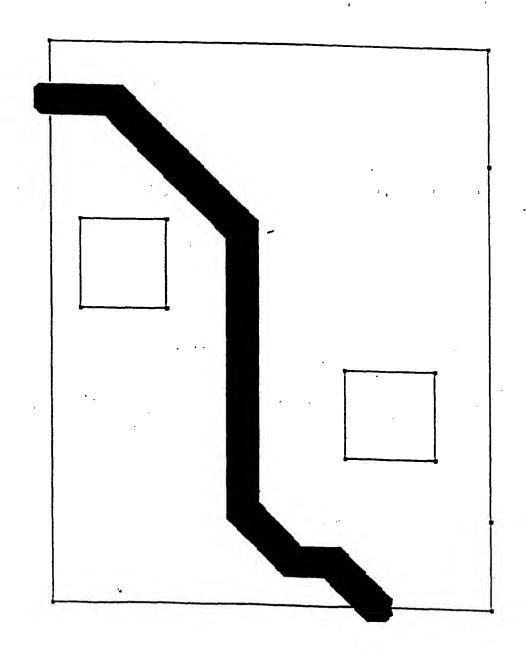
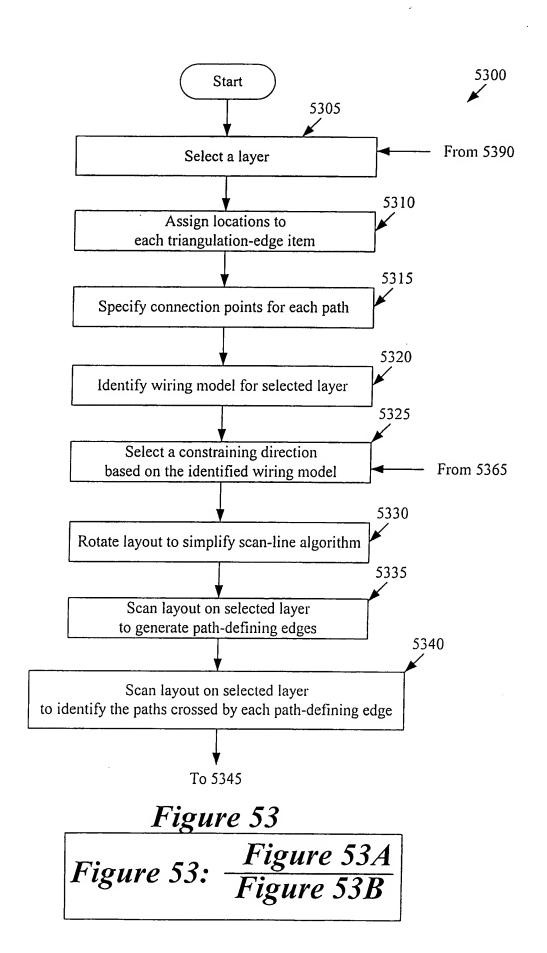
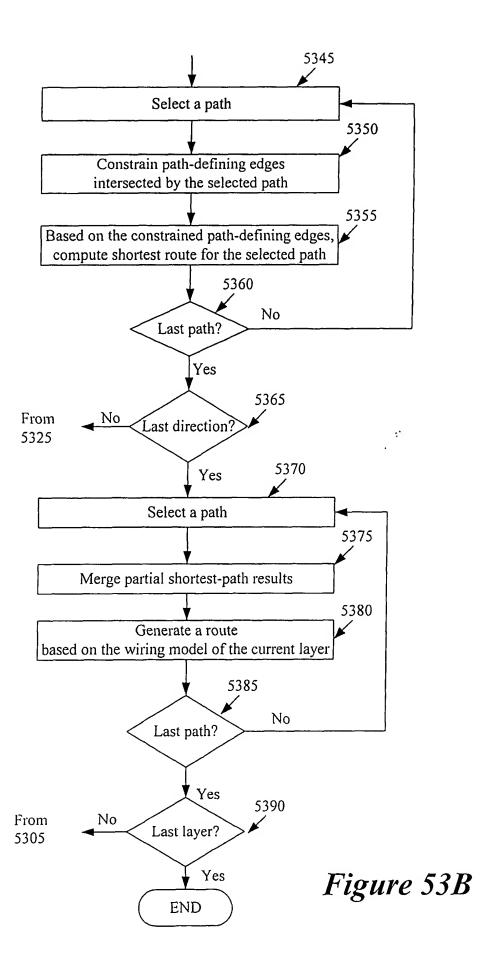
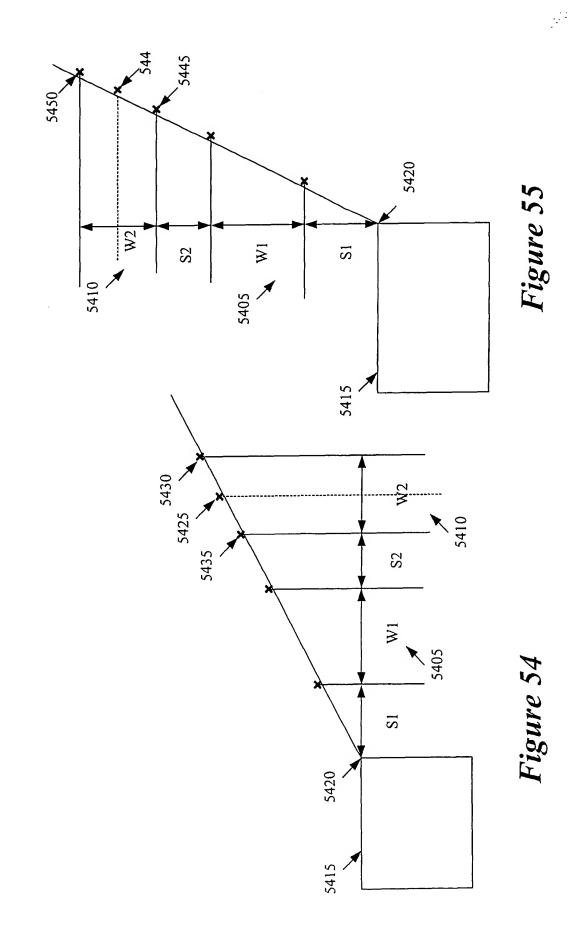
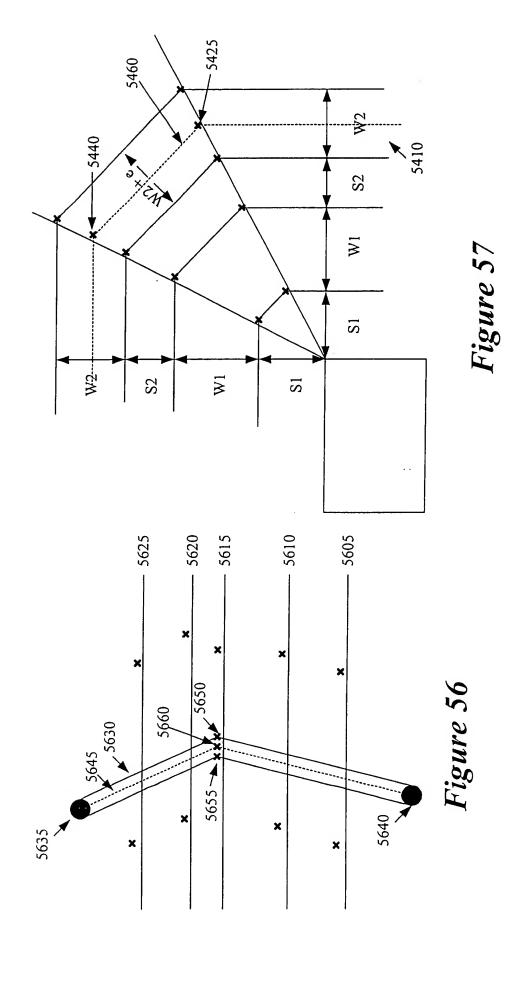


FIGURE 52









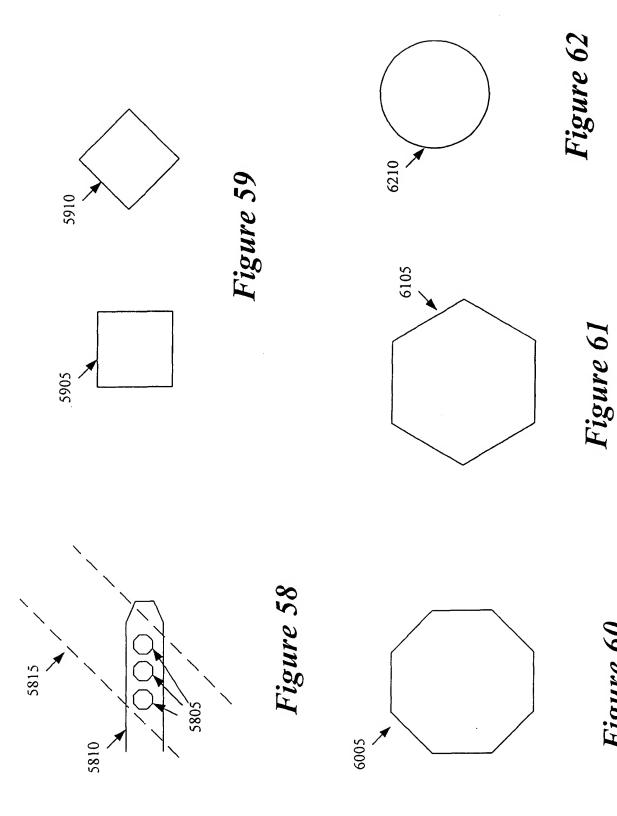


Figure 60

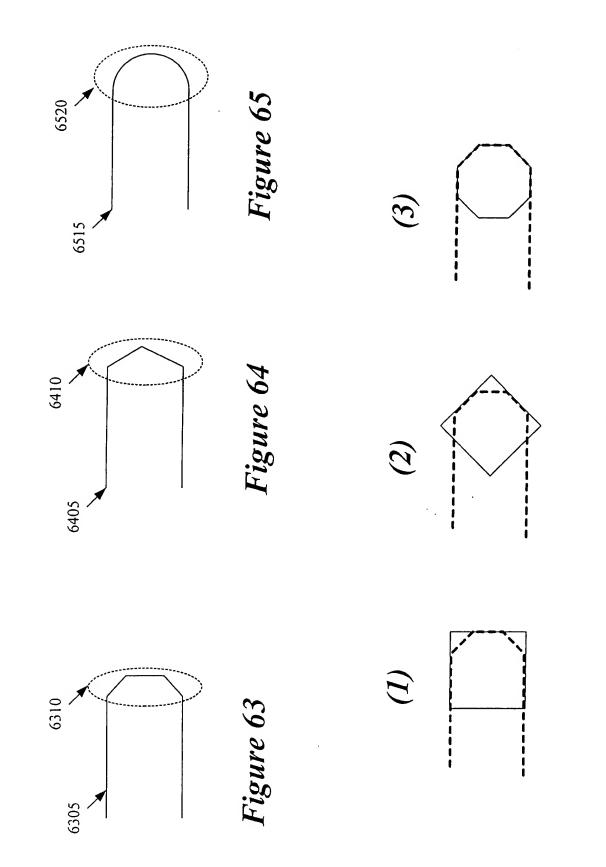
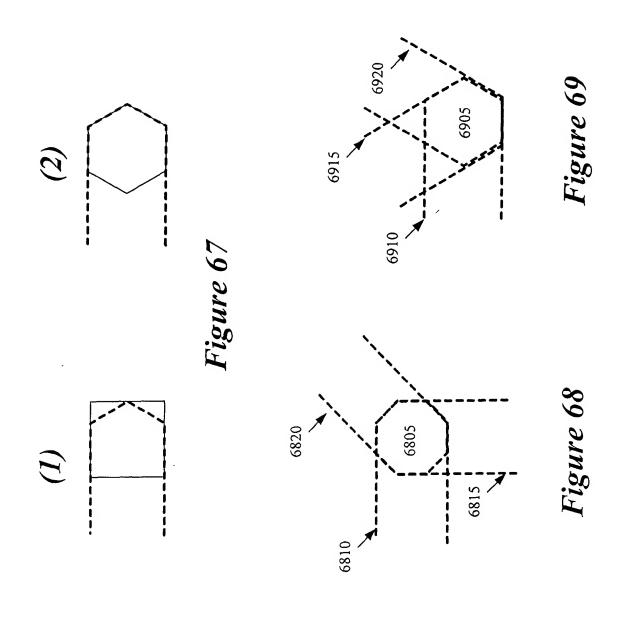
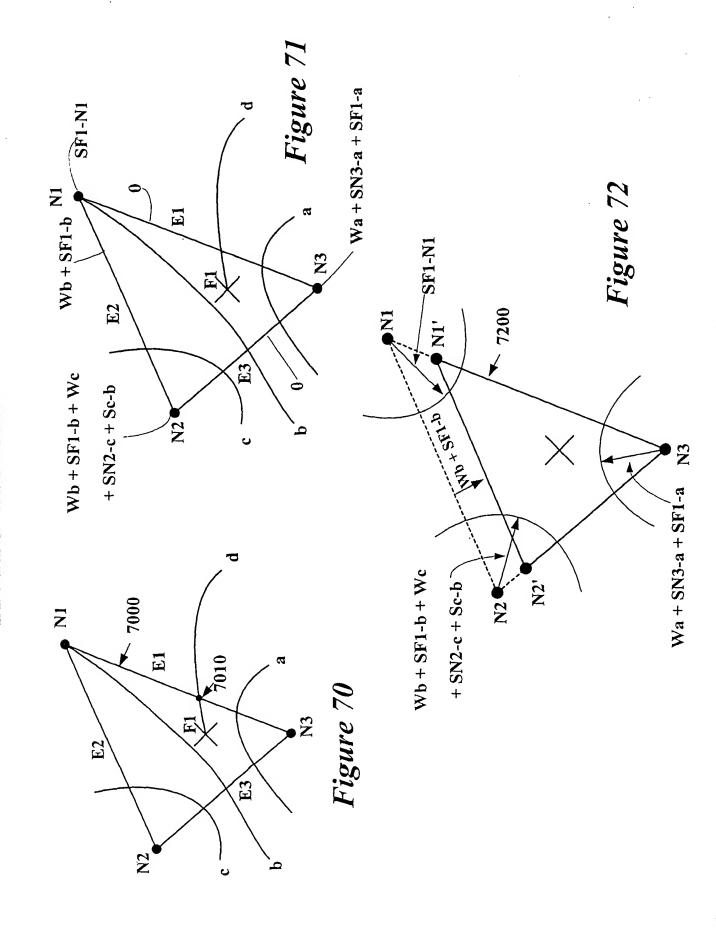
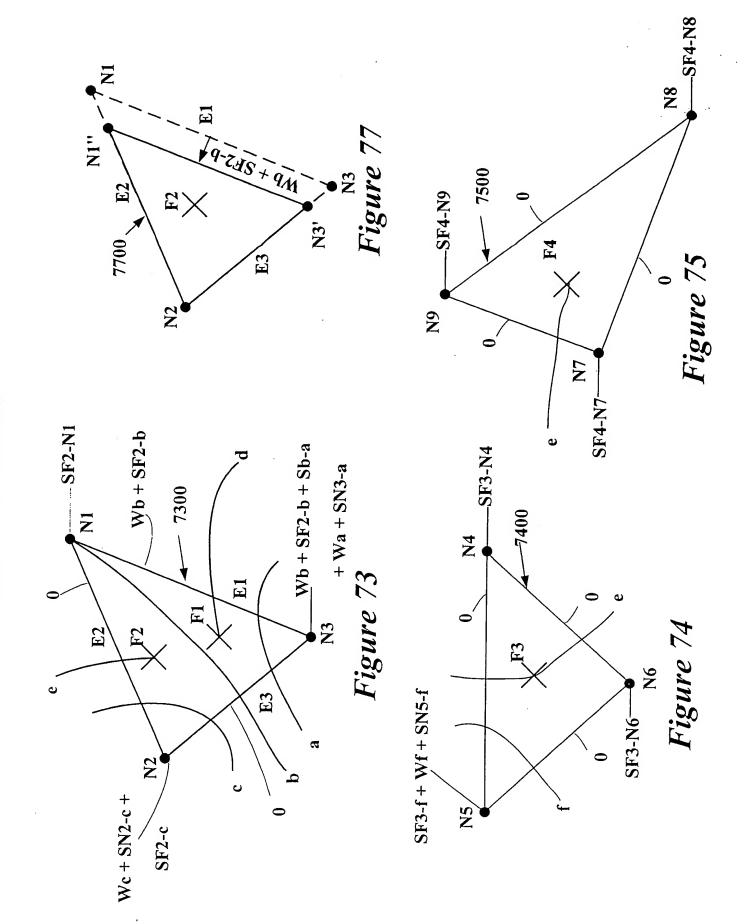
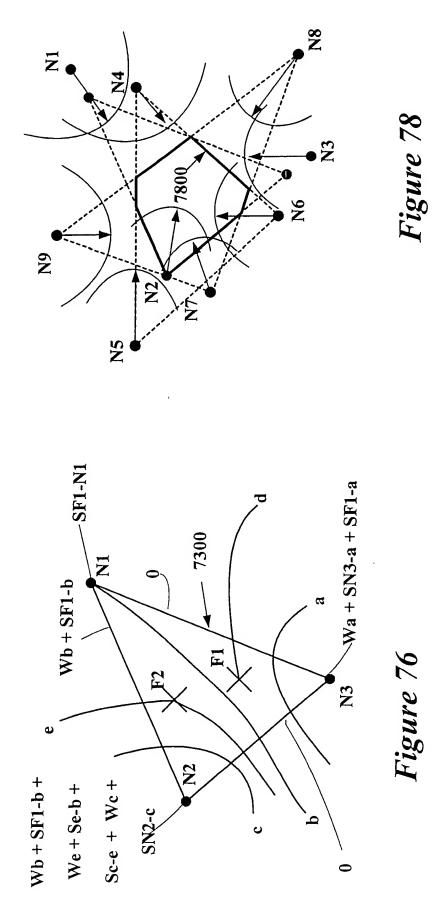


Figure 66









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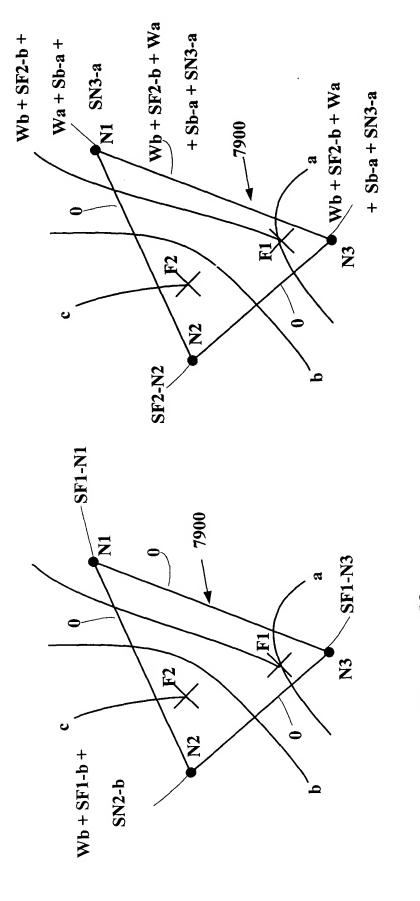


Figure 79

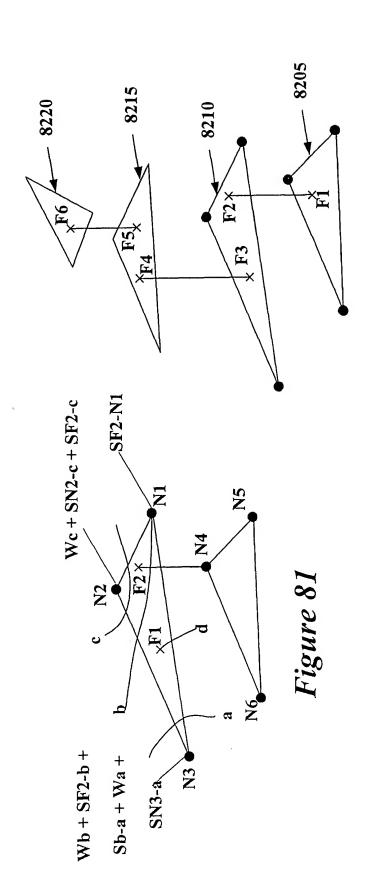


Figure 82

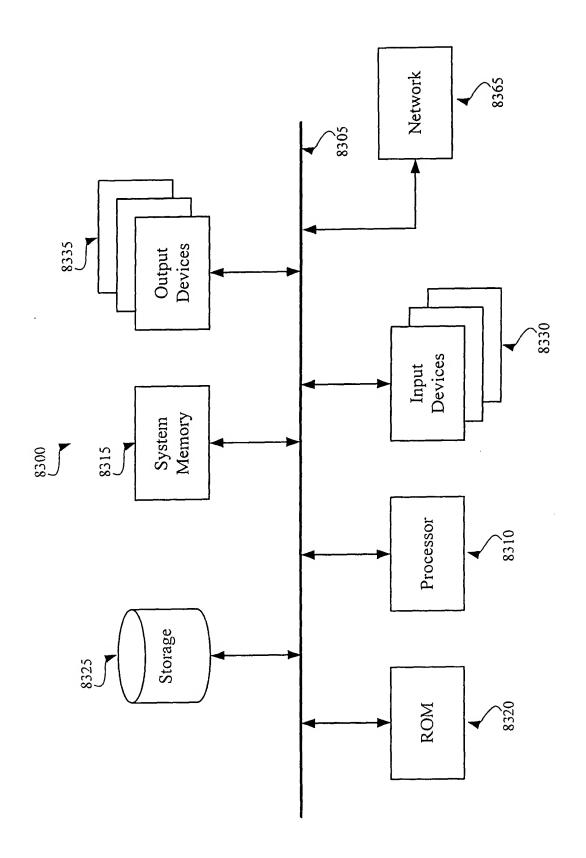


Figure 83